

STUDENT HANDBOOK

**BENG (HONS) ELECTRONICS & COMPUTER SYSTEMS
WITH DIS/DAS
BENG (HONS) ELECTRONICS & COMPUTER SYSTEMS**

PROGRAMME: 4231

SCHOOL OF COMPUTING AND INTELLIGENT SYSTEMS

FACULTY OF COMPUTING AND ENGINEERING

MAGEE CAMPUS

UNIVERSITY OF ULSTER

COURSES' CO-ORDINATOR: DR HEATHER SAYERS

SEPTEMBER 2009 ENTRY

PROGRAMME SPECIFICATION

COURSE OR HONOURS SUBJECT TITLE:

BEng (Hons) Electronics and Computer Systems with DIS
BEng (Hons) Electronics and Computer Systems with DAS
BEng (Hons) Electronics and Computer Systems

PLEASE NOTE: This specification provides a concise summary of the main features of the BEng Hons Electronics and Computer Systems (DIS) course and the learning outcomes that a typical student might reasonably be expected to achieve and demonstrate if he or she takes full advantage of the learning opportunities provided. More detailed information on the specific learning outcomes, content and the teaching, learning and assessment methods of each module can be found at <http://scis.ulster.ac.uk> and in the Student Handbook.

AWARD INSTITUTION/BODY:	UNIVERSITY OF ULSTER
TEACHING INSTITUTION:	UNIVERSITY OF ULSTER
LOCATION:	MAGEE
ACCREDITED BY:	BRITISH COMPUTER SOCIETY
FINAL AWARD:	BEng (Hons) Electronics and Computer Systems with DIS/DAS BEng (Hons) Electronics and Computer Systems
MODE OF ATTENDANCE:	Full-time and Part-time
SPECIALISMS:	Electronics and Computing
COURSE/UCAS CODE:	ULS U20 GH56 M BEng/EICom
DATE WRITTEN/REVISED:	2008/09

10. EDUCATIONAL AIMS AND OBJECTIVES OF THE COURSE

The course aims to prepare students for a career in professional electronic/computer engineering. It seeks to produce students with a high level of proficiency and a sound understanding of the integration of software and hardware system design. These skills will instil in the intended electronic and computer engineer a thorough knowledge of electronic engineering and computing principles and practice, together with an appreciation of the industrial or business environment in which they will work. At the end of their course, graduates of the BEng (Hons) Electronics and Computer Systems will have:

- An understanding of the fundamental principles underpinning the disciplines of electronic and computer engineering environments.
- A sound comprehension of the integration of electronic engineering and computing design procedures.
- Competence in the use of high and low level languages and software tools for the production of the software components.
- The ability to identify, specify and design reliable, cost-effective systems composed of hardware and software, to satisfy the needs of a commercial, industrial or administrative organisation.
- Capability in appreciating and interpreting contemporary developments in standards, protocols and network architectures.
- Familiarity with the instrumentation used in modern electronic and computer systems design and a proficiency in their utilisation.
- Studied selected areas of system specialisms through project work and final year options.
- Received sufficient knowledge of current electronic and computer engineering practice to enhance employment opportunities on graduation.

11 MAIN LEARNING OUTCOMES

The following reference points were used to inform the development of the programme and its learning outcomes:

- The QAA Computing subject benchmark statement (2000) (B);
- The British Computer Society Guidelines on Course Exemption and Accreditation (2001) (P);
- The University Qualifications and Credit framework;
- Computing Curricula 2005 (ACM/IEEE Computer Society);
- Current research and scholarship carried out by academic staff.

11.1 BEng Hons Electronics and Computer Systems with/without DIS/DAS

The course provides opportunities for students to achieve and demonstrate the following learning outcomes:

11.1K KNOWLEDGE AND UNDERSTANDING OF THE SUBJECT

Knowledge and understanding of:

- K1** Basic engineering and computing principles that are fundamental to computing and electronic Engineering (B,P).
- K2** The integration of software and hardware which is fundamental to electronic system design (B,P).
- K3** The economic, environmental, human and social impacts of computing and electronics (B).
- K4** The impact of computer systems and electronics in economic development and the engineering and information technology marketplace (B,P).
- K5** The tools enabling computer and electronic system design (B).

Learning and Teaching Methods: Lectures, tutorials, seminars and practical sessions.

Assessment Methods: Coursework, written unseen examinations

11.1I INTELLECTUAL QUALITIES

The ability to:

- I1** Analyse problems and design solutions (B).
- I2** Integrate electronic and computing theory with the available technologies (B,P).
- I3** Integrate information and data from a variety of sources (B,P).
- I4** Plan, conduct and report a programme of original research (B).
- I5** Apply computer science fundamentals to the comprehension and evaluation of advanced hardware and software technologies (B,P).

Learning and Teaching Methods: Lectures, tutorials, supervised practical sessions and self-directed learning, employing study packs and research based materials.

Assessment Methods: Coursework related to case studies and projects, written unseen examinations, workbooks, presentations, project reports and dissertation.

11.1P PROFESSIONAL / PRACTICAL SKILLS

The ability to:

- P1** Apply electronic and computing studies skills in the development and realisation of relevant case studies (B,P).
- P2** Design and carry out a programme of practical exercises using modern electronic system design technologies (B,P).
- P3** Prepare and interpret computer/electronic system problem specifications and reports.
- P4** Source and use technological information (B).
- P5** Utilise a range of engineering and computing tools across a variety of engineering applications (B,P).
- P6** Develop hands-on experience of basic electronics and computing technologies.

Learning and Teaching Methods: Lectures, tutorials, problem-based seminars and practical sessions, project preparation and implementation.

Assessment Methods: Placement report assessment (DIS only), problem-based coursework, workbooks, project reports and dissertation.

11.1T TRANSFERABLE/KEY SKILLS

The ability to:

- T1** Learn in both familiar and unfamiliar situations making effective use of information retrieval skills and learning (B).
- T2** Communicate effectively using various media and with a variety of audiences (B).
- T3** Apply numeracy in both understanding and presenting cases involving a quantitative aspect (B).
- T4** Apply engineering principles to engineering problems (B).
- T5** Manage one's own learning and development including time management, organisational skills and awareness of entrepreneurship issues (B,P).

Learning and Teaching Methods: Lectures, tutorials, seminars and practical sessions, project preparation and implementation.

Assessment Methods: Placement report assessment (DIS only), workbooks, project vivas, reports and dissertation.

11.1 PROGRAMME LEARNING OUTCOMES MAP – BEng Hons Electronics and Computer Systems / BEng Hons Electronics and Computer Systems with DIS/DAS																						
TITLES	CODE	K1	K2	K3	K4	K5	I1	I2	I3	I4	I5	P1	P2	P3	P4	P5	P6	T1	T2	T3	T4	T5
Level 3																						
Introduction to Internet and Multimedia Technology	COM008M1	√	√						√			√	√	√				√	√			√
Introduction to Programming	COM009M1	√	√				√	√	√			√	√	√					√			√
Introduction to Mathematics for Computing	COM010M1	√					√	√	√			√	√							√		√
Introduction to Electronics	COM011M2	√	√				√	√	√			√						√		√		√
Introduction to Computer Systems	COM012M2	√	√				√	√	√			√	√	√				√	√			√
Level 4																						
Business Information Systems	COM112M2	√	√					√	√			√	√	√				√	√			√
Algorithmic Programming I	COM136M1	√		√			√	√				√	√						√	√	√	
Mathematics 1	COM121M1	√					√					√	√			√		√		√	√	
Systems Analysis and Data Management	COM178M1	√		√			√	√					√	√					√		√	√
Computer Hardware and Organisation	COM167M2	√	√	√			√		√			√	√	√				√	√		√	
Algorithmic Programming II	COM139M2	√	√	√			√	√				√	√							√	√	
Analogue and Digital Electronics	COM119M2		√	√			√	√					√							√	√	
Level 5																						
Object Oriented Programming	COM326M1	√	√	√	√	√	√	√			√		√	√	√			√			√	
Professional Issues	COM417M1				√													√			√	√
Informatics Entrepreneurship	COM418M1				√									√					√			√
Mathematics II	COM420M1	√		√	√									√	√	√				√	√	
Data and Networked Communications	COM320M2		√			√	√	√	√					√	√	√			√		√	√
Electronic Systems Design	COM422M2		√	√		√	√	√			√				√	√		√	√			√
Microprocessor Systems Design	EEE315M2	√	√	√	√	√		√	√		√		√	√				√	√	√		

Level 5 (DIS)																						
Industrial Placement*	COM367M4			√	√						√	√		√			√		√			√
Level 6																						
Core:																						
Final Year Project	EEE521M4			√	√	√	√	√	√	√	√	√	√		√	√		√	√		√	√
Compulsory:																						
Visual Programming	COM622M1	√	√	√			√	√				√	√								√	√
Options:																						
Network Games Development	COM588M1		√	√		√	√	√	√			√	√	√		√			√		√	√
Intelligent Systems	COM623M1	√	√	√							√	√	√							√	√	
Mixed Signal Design	EEE546M1		√	√			√	√			√	√	√				√	√			√	
Programmable Logic Systems	EEE540M1	√	√	√			√	√			√	√	√	√	√	√			√	√	√	
Embedded Systems Design	EEE502M2		√	√	√						√				√	√		√	√		√	
Digital Signal Processing	EEE523M2	√	√	√	√			√	√				√	√			√			√		
Wireless Communication Systems	COM586M2	√	√	√		√					√	√	√						√	√	√	

*NOTE: The Diploma in Area Studies (DAS) module is organised at University level.

11.2 Associate Bachelor in Electronics and Computer Systems with DIS/DAS Associate Bachelor in Electronics and Computer Systems

The course provides opportunities for students to achieve and demonstrate the following learning outcomes:

11.2K KNOWLEDGE AND UNDERSTANDING OF THE SUBJECT

Knowledge and understanding of:

- K1** Basic engineering and computing principles that are fundamental to computing and electronic engineering (B,P).
- K2** The integration of software and hardware which is fundamental to electronic system design (B,P).
- K3** The economic, environmental, human and social impacts of computing and electronics (B).
- K4** The tools enabling computer and electronic system design (B).

Learning and Teaching Methods: Lectures, tutorials, seminars and practical sessions.

Assessment Methods: Coursework, written unseen examinations.

11.2I INTELLECTUAL QUALITIES

The ability to:

- I1** Analyse problems and design solutions (B).
- I2** Integrate electronic and computing theory with the available technologies (B,P).
- I3** Integrate information and data from a variety of sources (B,P).
- I4** Apply computer science fundamentals to the comprehension and evaluation of hardware and software technologies (B,P).

Learning and Teaching Methods: Lectures, tutorials, supervised practical sessions and self-directed learning.

Assessment Methods: Coursework related to case studies and projects, written unseen examinations, workbooks, presentations.

11.2P PROFESSIONAL / PRACTICAL SKILLS

The ability to:

- P1** Apply electronic and computing studies skills in the development of relevant case studies (B,P).
- P2** Design and carry out a programme of practical exercises using modern electronic system design technologies (B,P).
- P3** Prepare and interpret computer/electronic system problem specifications and reports.
- P4** Source and use technological information (B).
- P5** Utilise a range of engineering and computing tools across a variety of engineering applications (B,P).
- P6** Develop hands-on experience of basic electronics and computing technologies.

Learning and Teaching Methods: Lectures, tutorials, problem-based seminars and practical sessions, project preparation and implementation.

Assessment Methods: Placement report assessment (DIS only), problem-based coursework, workbooks.

11.2T TRANSFERABLE/KEY SKILLS

The ability to:

- T1** Communicate effectively using various media and with a variety of audiences (B).
- T2** Apply numeracy in both understanding and presenting cases involving a quantitative aspect (B).
- T3** Apply engineering principles to engineering problems (B).
- T4** Manage one's own learning and development including time management, organisational skills and awareness of entrepreneurship issues (B,P).

Learning and Teaching Methods: Lectures, tutorials, seminars and practical sessions, project preparation and implementation.

Assessment Methods: Placement report assessment (DIS only), workbooks.

11.2	PROGRAMME LEARNING OUTCOMES MAP - Associate Bachelor in Electronics and Computer Systems with DIS/DAS Associate Bachelor in Electronics and Computer Systems																		
TITLES	CODE	K1	K2	K3	K4	I1	I2	I3	I4	P1	P2	P3	P4	P5	P6	T1	T2	T3	T4
Level 3																			
Introduction to Internet and Multimedia Technology	COM008M1	√	√					√		√	√	√				√	√		√
Introduction to Programming	COM009M1	√	√			√	√	√		√	√	√					√		√
Introduction to Mathematics for Computing	COM010M1	√				√	√	√		√	√							√	√
Introduction to Electronics	COM011M2	√	√			√	√	√		√						√		√	√
Introduction to Computer Systems	COM012M2	√	√			√	√	√		√	√	√				√	√		√
Level 4																			
Business Information Systems	COM112M2	√	√				√	√		√	√	√				√	√		√
Algorithmic Programming I	COM136M1	√		√		√	√			√	√					√	√	√	
Mathematics 1	COM121M1	√				√				√	√			√			√	√	
Systems Analysis and Data Management	COM178M1	√		√		√	√				√	√				√		√	√
Computer Hardware and Organisation	COM167M2	√	√	√		√		√		√	√	√				√		√	
Algorithmic Programming II	COM139M2	√	√	√		√	√			√	√						√	√	
Analogue and Digital Electronics	COM119M2		√	√		√	√				√						√	√	
Level 5																			
Object Oriented Programming	COM326M1	√	√	√	√	√	√		√		√	√	√					√	
Professional Issues	COM417M1																	√	√
Informatics Entrepreneurship	COM418M1											√				√	√		√
Mathematics II	COM420M1	√		√								√	√	√				√	
Data and Networked Communications	COM320M2		√		√	√	√	√				√	√	√		√		√	√
Electronic Systems Design	COM422M2		√	√	√	√	√		√				√	√		√			√
Microprocessor Systems Design	EEE315M2	√	√	√	√		√	√	√		√	√				√	√		

Level 5 (DIS only)																			
Industrial Placement*	COM367M4			√					√	√		√			√	√			√

*NOTE: The Diploma in Area Studies (DAS) module is organised at University level.

11.3 Diploma in Computer Systems (Exit Award)

11.3 MAIN LEARNING OUTCOMES

Level 6 provides opportunities for students to achieve and demonstrate the following learning outcomes. Successful students will be able to:

11.3K KNOWLEDGE AND UNDERSTANDING OF SUBJECT

- K1** Demonstrate knowledge and understanding of basic facts, concepts, principles, and practices underlying computer and electronics systems;
- K2** Use such knowledge and understanding in the design and development of electronics and software solutions for the purposes of comprehension, and the understanding of trade-offs;
- K3** Evaluate the extent to which an existing solution meets a defined need and understand how an electronics or software solution could be applied to enhance it;

Teaching and Learning Methods: Lectures, tutorials, seminars and practical sessions.

Assessment Methods: Coursework, written unseen examinations, technical presentations.

11.3I INTELLECTUAL QUALITIES

- I1** Demonstrate the ability to specify, design and construct simple electronics and software solutions;
- I2** Use this ability to evaluate such systems with respect to general quality and possible trade-offs within the parameters of the problem;
- I3** Demonstrate the ability to recognise the implications or risks involved in the operation of computing equipment within an ergonomic context;
- I4** Demonstrate the ability to generate and evaluate the results of tests which investigate the effective operation of an electronics and software solution.

Teaching and Learning Methods: Lectures, tutor-directed tutorials, supervised practical sessions and self-directed learning.

Assessment Methods: Coursework related to case studies and mini-projects, written unseen examinations, presentations.

11.3P PROFESSIONAL/PRACTICAL SKILLS

- P1** Select and apply simple design methodologies to a given problem domain;
- P2** Employ effectively the tools used for the construction and documentation of electronics and software solutions;
- P3** Prepare and deliver presentations and written reports.

Teaching and Learning Methods: Lectures, tutorials, problem-based seminars and practical sessions, and mini-projects.

Assessment Methods: Problem-based coursework, workbooks, and reports.

11.3T TRANSFERABLE SKILLS

- T1** Learn in both familiar and unfamiliar situations making effective use of information-retrieval skills and of learning resources;
- T2** Communicate effectively using various media and with a variety of audiences, using rational and reasoned arguments either orally, written or electronically;
- T3** Apply numeracy in presenting cases involving a quantitative aspect;
- T4** Manage one's own learning and development including time management and organisational skills.

Teaching and Learning Methods: Lectures, tutorials, seminars, practical sessions, and mini-projects.

Assessment Methods: Workbooks, presentations and reports.

11.3 PROGRAMME LEARNING OUTCOMES MAP Diploma in Computer Systems (Exit Award)

Please note: The matrix displays only the measurable programme outcomes and where these are developed and assessed within the modules offered in the programme.

MODULE TITLES	OUTCOMES														
	CODE	K1	K2	K3	I1	I2	I3	I4	P1	P2	P3	T1	T2	T3	T4
Level 3															
Introduction to Internet and Multimedia Technology	COM008M1	√	√	√			√		√	√	√	√	√		√
Introduction to Programming	COM009M1	√	√	√	√	√	√	√	√	√	√		√		√
Introduction to Mathematics for Computing	COM010M1	√			√	√	√	√	√	√				√	√
Introduction to Electronics	COM011M2	√	√	√	√	√	√	√	√			√		√	√
Introduction to Computer Systems	COM012M2	√	√	√	√	√	√		√	√	√	√	√		√
Level 4															
Business Information Systems	COM112M2	√	√	√		√	√		√	√	√	√	√		√

12 STRUCTURE AND REQUIREMENTS FOR THE AWARD

The BEng (Hons) Electronics and Computer Systems course is of four/five years' duration depending on point of entry (Level 3: 5; Level 4: 4). Entry is normally into Level 4 of the course. Entry is permitted into Level 5 in exceptional circumstances. Years 1, 2 and 4 are spent at the university and Year 3 is a period of industrial placement.

The course structure is centred on three broad themes: Electronics, Programming and Embedded and Computing Systems.

The main aim of Level 3 entry is to widen access to honours degree level courses in computing and electronics. Level 3 therefore aims to provide introductory knowledge in computer systems and electronics, and thus prepare students to proceed onto the normal (Level 4) programme of study for the honours degree course. Additionally, it also aims to equip those students who decide to finish their studies at Diploma level with fundamental knowledge and skills that enable them to pursue a productive professional career in the computing or electronics industries. Level 3 consists of 120 points.

Years 1 and 2 consist of a set of compulsory modules addressing these themes, enabling students to achieve the basic competencies in hardware/software system design and equipping them for a period of industrial placement. Year 1 consists of 120 points at Level 4 and Year 2 also consists of 120 points at Level 5.

All students normally spend Year 3 on placement, working in industry for a minimum period of 26 weeks. On satisfactory completion of the placement period the student is eligible for the award of Diploma in Industrial Studies (DIS). A student who satisfactorily completes Year 3 in study abroad is eligible for the award of Diploma in Area Studies (DAS). In exceptional circumstances, at the discretion of the Dean, students may be allowed to proceed to Level 6 without completing placement. Those students would not receive the award of DIS/DAS.

Year 4 consists of one compulsory 30-point project module, one compulsory 10-point module and four optional modules, all at Level 6. The compulsory module (Project) enables the student to engage in a substantive piece of work comprising both theoretical and practical elements at an advanced level. A range of optional modules is offered. Students may elect to do a broad range of topics or specialise in one or two designated areas, such as Embedded Systems, Digital Signal Processing or Intelligent Systems.

Satisfactory completion of each pre-final year of the course is normally a pre-requisite for progression to the subsequent year. Satisfactory completion of year 4 leads to the award of the degree with honours. Pass requirements and honours classifications are detailed in section 16 below.

Students who, for whatever reason/s, are unable to satisfy the University requirements for a Level 6 qualification, but who have passed all elements at Level 5 and optionally the DIS year, will have fulfilled the learning outcomes commensurate with the intermediate Level 6 Associate Bachelor (AB) exit award (either the AB award or the AB award with Diploma in Industrial Studies).

BEng (Hons) Electronics and Computer Systems with DIS/DAS
BEng Hons Electronics and Computer Systems

Year 1 (Level 4) All modules are compulsory	20	COM121 Mathematics I	20	COM178 Systems Analysis & Data Management	20	COM136 Algorithmic Programming I
	20	COM167 Computer Hardware & Organisation	20	COM119 Analogue & Digital Electronics	20	COM139 Algorithmic Programming II

Year 2 (Level 5) All modules are compulsory	20	COM420 Mathematics II	10	COM417 Professional Issues	20	COM326 Object Oriented Programming
			10	COM418 Informatics Entrepreneurship		
	20	COM320 Computer Networks and Operating Systems	20	COM422 Electronics Systems Design	20	EEE315 Microprocessor Systems Design

Year 3 (Level 5) (DIS/DAS only)	60					
	COM367 DIS Placement OR DAS Placement (organised at University level)					

Y E A R 4		20 Optional	10 Compulsory	20 Optional	20 Optional
		COM588 Network Games Development	COM622 Visual Programming	COM623 Intelligent Systems	EEE540 Programmable Logic Systems
L E V E L 6	20 Optional	30 Core	20 Optional	20 Optional	20 Optional
	COM596 Mobile Robotics	EEE521 Final year Project**	EEE502 Embedded Systems Design	COM561 Concurrent and Distributed Systems	COM586 Wireless Communication Systems

The CAT value/level is shown in the top left corner of each module. Modules in Year 4 are indicated as COMPULSORY or OPTIONAL. Students must choose modules to a total of 60 CAT points in each Semester in the Final Year.

**The project module runs over both semesters: 10 points in Semester 1 and 20 points in semester 2.

Associate Bachelor in Electronics and Computer Systems with DIS/DAS Associate Bachelor in Electronics and Computer Systems

Level 3 All modules are compulsory	20	COM008 Introduction to Internet and Multimedia Technology	20	COM009 Introduction to Programming	20	COM010 Introduction to Mathematics for Computing
	20	COM011 Introduction to Electronics	20	COM012 Introduction to Computer Systems	20	COM112 Business Information Systems
Year 1 (Level 4) All modules are compulsory	20	COM121 Mathematics I	20	COM178 Systems Analysis & Data Management	20	COM136 Algorithmic Programming I
	20	COM167 Computer Hardware & Organisation	20	COM119 Analogue & Digital Electronics	20	COM139 Algorithmic Programming II
Year 2 (Level 5) All modules are compulsory	20	COM420 Mathematics II	10	COM417 Professional Issues	20	COM326 Object Oriented Programming
			10	COM418 Informatics Entrepreneurship		
	20	COM320 Computer Networks and Operating Systems	20	COM422 Electronics Systems Design	20	EEE315 Microprocessor Systems Design
Year 3 (Level 5) (DIS/DAS only)	60	COM367 DIS Placement OR DAS Placement (organised at University level)				

Diploma in Computer Systems (Exit Award)

Level 3 All modules are compulsory	20	COM008 Introduction to Internet and Multimedia Technology	20	COM009 Introduction to Programming	20	COM010 Introduction to Mathematics for Computing
	20	COM011 Introduction to Electronics	20	COM012 Introduction to Computer Systems	20	COM112 Business Information Systems

The following support is provided for students and their learning:

- An Induction Programme for new students (all years) is held throughout the first weeks of each semester. This Induction Programme includes talks by the Courses' Coordinator, the PDP Coordinator, the Placement Tutor, and includes tours of the Learning Resource Centre (LRC) given by LRC staff.
- The Courses' Coordinator is available at specified hours for advice.
- A Course website provides an electronic version of the Student Handbook (including this document), an electronic notice board with daily notices and links to other on-line resources (Academic Affairs, Careers, Library, timetables etc).
- Students are supported by extensive library and other learning resources.
- The School of Computing and Intelligent Systems is resourced by a number of state-of-the-art computer laboratories with a wide range of software. New software is introduced as required per semester.
- All modules have an associated website with on-line learning support material (module specification, lecture notes, useful web links etc).
- Every student is allocated an email account and has full (24hours, 7 days a week) access to the computing laboratories and the Internet.
- Each student is allocated a personal Studies Advisor who is available for academic and academic-related advice.
- Placement units are dedicated to preparing students for placements and finding placement opportunities for students.
- Students are visited on placement by academic staff.
- Advertised availability hours for academic staff.
- Course notice board.
- Every student receives a hard copy of the Student Handbook (incorporating this document) which contains details of the following university services:
- Career Development Centre
- Information Services Department
- Student Support Department
- Sport and Recreation Department
- International Office (for international students)
- Students' Union
- Chaplaincy

Applicants must satisfy the University's general entry requirements as set out in the prospectus or demonstrate their ability to undertake the course through the accreditation of prior experiential learning (APEL). The initial offer standard may vary from year to year. See prospectus entry.

Year 2 Entry: Direct entry to Year 2 is only possible for suitably qualified candidates. Entry to the part-time course is at Level 5 (Year 2) only.

Mechanisms for review and evaluation of teaching, learning, assessment, the curriculum and outcome standards:

- Initial validation and regular revalidation exercises.
- Module reviews (including student input).
- Annual staff reviews.
- Regular Course Committee reviews.
- Course accreditation by external bodies.
- Annual Subject Monitoring report prepared by the Courses' Coordinator.
- Review of external examiner views expressed in assessment moderation, during Examination Board visits and in annual reports.
- Peer teaching observations and feedback.
- Review of employer views expressed through annual feedback surveys of employers of placement students and at meetings of the Faculty Industrial Liaison Panel.
- The National Student Survey.
- Student performance data and career progression.

Committees with responsibility for monitoring and evaluating quality:

- Staff Student Consultative Committee.
- Course Committee.
- Board of Examiners.
- School Board (includes student members).
- Faculty Academic Affairs Committee (includes student members).
- University Teaching and Learning Committee.

Mechanisms for gaining student feedback on the quality of their learning experience

- Staff-Student Consultative Committee.
- Student representatives on School and Faculty boards.
- Student Questionnaires.
- Placement reports.

Staff development includes:

- Updating in the subject through research and scholarship.
- Membership of professional bodies.
- University Staff Development Programme.
- Membership of the Higher Education Academy.
- Consultancy.
- Research and Knowledge Transfer.
- Representation on external working groups and committees.

16 REGULATION OF STANDARDS

Assessment rules

- The pass mark for the course, modules, and individual assessments is 40%
- Degree classifications:
 - 1st class: Overall mark of 70% or above.
 - 2:1: Overall mark of 60% and <70%.
 - 2:2: Overall mark of 50% and <60%.
 - 3rd class: Overall mark of 40% and <50%.
- Honours classification derives 100% from Level 6
- The award of DIS/DAS requires a minimum mark of 50% in the placement year. DIS/DAS with Commendation is awarded for marks $\geq 70\%$.

External examiners

The course has one external examiner. His/her role is to consult, through the Courses' Co-ordinator, with the internal examiners on the approval and moderation of examination papers and other forms of assessment. The external examiner approves the coursework and examination papers for Level 3 and Level 6, and reviews all the assessment marking and results for Levels 4 and 5. In addition, the external examiner ensures comparability with similar courses at other institutions as regards course content and the standard of marking of examination papers and other forms of assessment. The external examiner provides valuable feedback to the Course Committee by submitting annual reports outlining any strengths or weaknesses of the course and the assessment procedures. The external examiner may assist the Course Committee in reaching decisions on borderline candidates, and may subject such candidates to viva-voce examination.

17 INDICATORS OF QUALITY RELATING TO TEACHING AND LEARNING

- The course is accredited by the British Computer Society.
- A large number of staff are members of the Higher Education Academy.
- All Computing courses were subject to a Faculty Subject Review during 2002, and participated in a Discipline Audit Trail (DAT) during an Institutional audit in 2005.
- In the 2001 Research Assessment Exercise the Faculty of Informatics (subsequently Engineering) achieved a grade 4 in Unit of Assessment 25 (Computer Science).
- All staff hold (or if not, are required to achieve) a postgraduate certificate in university teaching (PGCUT/PGCHET).
- The School has attracted external funding for teaching and learning initiatives, including the Wireless Broadband project (2003-2006).

Placement and Career Opportunities

Students spend Year 3 in relevant and supervised employment referred to as industrial placement. The first two years of the Programme are designed to provide the students with the knowledge and expertise they will need to extract maximum benefit from the placement experience.

Preparation for Placement/Employment

In addition to exploring the various aspects of the placement year and its relationship with other elements of the Programme, students are prepared for the application and selection procedures associated with placement and ultimately, employment. This includes advice on researching the market, completion of application forms, and preparation for interview. Much of this preparation takes place in Year 2 during a 'Placement Preparation' module (not formally timetabled). However, all Year 1 and 2 modules contribute to the desired portfolio of skills and in particular, the teamwork aspects will help to develop important inter-personal and group skills that are of increasing importance within the commercial environment.

There is a wide range of placement opportunities in Northern Ireland, Great Britain and the Republic of Ireland, and from time to time some placements overseas. We will find several employers willing to consider you for placement. Selection is by competitive interview by the employer concerned. They will pay your interview expenses. There is no maintenance grant or loan during the placement year; instead you will receive a proper salary. The placement lasts for about 48 weeks. Students have reported in glowing terms on the value of the placement experience, and their placement employers have offered several eventual permanent positions. Similarly placement employers have reported very favourably on our students.

For the student, placement should ideally provide appropriate training and work based in a well organised team with a significant element of computing. It must provide a genuine opportunity for the student to develop towards professional competence. The Faculty of Computing and Engineering has built up contacts with many employing organisations that are in a position to provide suitable placements. While the precise nature of placements will vary from employer to employer, it is intended that students are initially given close supervision and guidance and are progressively given more responsibility as the placement proceeds. Before the end of placement students should be contributing as a full employee of the company. During placement each student is supervised by an industrial supervisor from the employing company and an academic supervisor from the Faculty of Computing and Engineering.

Normally, each student will receive at least two visits from an academic supervisor. However, special arrangements such as telephone, e-mail or video conferencing may be used to contact those students who are placed in geographically distant locations. These media may also be used to supplement the formal academic visits to other students if necessary.

The experience gained and the associated personal developments are subsequently drawn on during the final year of the BEng Programme. In some cases the final year project may stem directly from projects that have been identified during the placement year. Successful completion of the industrial placement is recognised by the award of the Diploma in Industrial Studies. Placements also help the Faculty to provide graduates with the qualities that employers require. The visits of academic supervisors to students on placement provide an ideal opportunity to obtain a view of the Programmes from an industrial perspective. The links with industry, which placement promotes, also help the Course Committee to keep abreast of changing industrial requirements and often lead to other joint ventures such as custom-built training Programmes, Knowledge Transfer Partnerships and other collaborative research.

Placement offers an opportunity to use and enhance the skills developed during the first two years of the Programme in a work-based context. Each student's placement experience will be different depending on the employer but each placement is vetted by the University to ensure it offers a worthwhile experience. Within the Faculty, there have been many instances of students taking up a full-time post with their placement employer after graduation.

In addition to developing 'computing' skills, placement often provides opportunities for students to acquire other transferable skills such as working as part of a team, interacting with clients, business awareness etc.

“Key skills” are qualities of graduates that are greatly desired by today’s employers. They include the ability to write written reports or give oral presentations. Opportunities are provided throughout the Programme to develop these important qualities, in particular through group assignment work included in several modules and the opportunities offered through placement. Often assignments will include marking criteria that assess a student’s ability in one or more key skills, for example, by awarding a mark for the effectiveness of the individual as part of a group project.

The Placement Tutor

The BSc Programme has a member of academic staff known as a Placement Tutor who is responsible for:

- Establishing and maintaining good relations with placement providers;
- Finding new placements as required;
- Preparing students for the placement experience;
- Administering the process whereby students are placed;
- Collating the assessment returns at the end of the placement year;
- Making recommendation regarding each student’s overall performance and progress.
- The Programme Placement Tutor is a member of a Faculty of Computing and Engineering Placement Group chaired by a Faculty Placement Co-ordinator. This group seeks to ensure close collaboration in the work of various Programme placement tutors.

Career Opportunities

Students from this Programme may enter a broad range of possible careers. For example, they may go on to become technical software developers, Web application designers, HCI consultants or systems analysts. Further study routes are also possible through MRes, MPhil and PhD Programmes for those wishing to conduct detailed research into specific aspects of computer science.

Careers advice is formally provided by the Careers Service. See <http://www.ulst.ac.uk/careers/> for more information.

THE PROGRAMME TIMETABLE AND WEBSITE

The Programme timetable will be distributed at enrolment or induction and can also be viewed via the School of Computing and Intelligent System’s website at: <http://scis.ulster.ac.uk>. This website provides a repository of information for all the undergraduate courses at Magee and links to all the relevant on-line resources.

DATES OF ATTENDANCE/EXAMINATIONS/VACATIONS : 2009/10

Semester 1

Monday 21 September 2009	Teaching begins (Autumn)
Friday 11 December 2009	Teaching ends
Monday 14 December 2009	Christmas Vacation begins
Friday 25 December 2009 to Friday 1 January 2010	University Closed (Christmas)
Friday 4 January 2010	Christmas Vacation ends
Tuesday 5 January to Saturday 16 January 2010	Examination Period
Friday 22 January 2010	Autumn Semester ends
Friday 5 February 2010	Last date for meetings of Course/Subject Committees (Semester One Progress review)

Semester 2

Monday 25 January 2010	Teaching begins (Spring)
Wednesday 17 March 2010	University Closed (St Patrick's Day)
Monday 29 March 2010	Easter Vacation begins
Monday 5 April to Friday 9 2010	University Closed
Friday 9 April 2010	Easter Vacation ends
Monday 3 May 2010	University Closed (May Day)
Tuesday 4 May to Friday 7 May 2010	Revision week (non-teaching)
Monday 10 May to Saturday 22 May 2010 (with possible extension to 25 May if required for first sit examinations)	Examination period
Friday 28 May 2010	Spring semester ends
Thursday 10 June 2010	Last date for meetings of Boards of Examiners
Monday 28 June – Tuesday 6 July 2010	Summer Graduation Ceremonies
Resit Period Wednesday 11 August to Thursday 19 August 2010	Supplementary Examinations
Monday 23 August to Friday 27 August 2010	Meetings of Boards of Examiners
Semester 3 Monday 19 July 2010	Teaching begins (Summer)
Monday 30 August to Friday	Examination Period

3 September 2010

Friday 10 September 2010

Monday 13 September to Friday
17 September 2010

Summer Semester ends

Meetings of Boards of Examiners

PROGRAMME MANAGEMENT

Courses' Co-ordinator (Course Director for all Undergraduate Courses)

Name	Office	Email	Telephone	Comments
Dr Heather Sayers	MG121C	hm.sayers@ulster.ac.uk	75148	Senior Lecturer & Courses' Coordinator

The Teaching Team consists of:

Name	Office	Email	Telephone	Comments
Professor Liam Maguire	MS127	lp.maguire@ulster.ac.uk	75605	Head of School
Professor Paul McKeivitt	MS112	p.mckeivitt@ulster.ac.uk	75433	Professor of Digital Multimedia
Professor Ulrich Nehmzow	MS205	u.nehmzow@ulster.ac.uk	75059	Professor of Cognitive Robotics
Dr Laxmidhar Behera	MS107	l.behera@ulster.ac.uk	75276	Reader
Dr Shyam Chakraborty	MS135	ss.chakraborty@ulster.ac.uk	75126	Reader
Dr Girijesh Prasad	MS137	g.prasad@ulster.ac.uk	75645	Reader
Dr Kevin Curran	MS130	kj.curran@ulster.ac.uk	75565	Senior Lecturer
Dr Tom Lunney	MG121D	tf.lunney@ulster.ac.uk	75388	Senior Lecturer
Dr Liam McDaid	MS016	lj.mcdaid@ulster.ac.uk	75452	Senior Lecturer
Mr Derek Woods	MS134	dn.woods@ulster.ac.uk	75380	Senior Lecturer
Dr Ammar Belatreche	MS104	a.belatreche@ulster.ac.uk	75158	Lecturer
Mr Michael Callaghan	MS132	mj.callaghan@ulster.ac.uk	75771	Lecturer
Dr Sonya Coleman	MS133	sa.coleman@ulster.ac.uk	75030	Lecturer
Dr Joan Condell	MS131	j.condell@ulster.ac.uk	75024	Lecturer
Mr Martin Doherty	MG121A	m.doherty@ulster.ac.uk	75552	Lecturer
Dr Peter Kelly	MG121E	pm.kelly@ulster.ac.uk	75293	Lecturer
Mr Aiden McCaughey	MG126	a.mccaughey@ulster.ac.uk	75131	Lecturer & Placement Officer
Dr Sandra Moffett	MS015	sm.moffett@ulster.ac.uk	75381	Lecturer
Mrs Mairin Nicell	MG127	ma.nicell@ulster.ac.uk	75007	Lecturer
Mr Maeve Paris	MG040	m.paris@ulster.ac.uk	75212	Lecturer
Dr Jose Santos	MG035	ja.santos@ulster.ac.uk	75034	Lecturer
Dr NH Siddique	MG037	nh.siddique@ulster.ac.uk	75340	Lecturer
Dr Zumao Weng	MG050	zm.weng@ulster.ac.uk	75358	Lecturer
Dr Shane Wilson	MG038	s.wilson@ulster.ac.uk	75527	Lecturer

Support Staff:

Name	Office	Email	Telephone	Comments
Dr Caitriona Carr	MG121B	c.carr@ulster.ac.uk	75784	Computer Training Officer
Mr Anthony McCourt	MG121B	a.mccourt@ulster.ac.uk	75784	Computer Training Officer
Mr Pratheepan Yogarajah	MS216	p.yogarajah@ulster.ac.uk	75171	Teaching Assistant
Mr Ted Leath	MG126	ta.leath@ulster.ac.uk	75366	Senior Computing Officer
Mr Paddy McDonough	MS034	p.mcdonough@ulster.ac.uk	75322	Computing Officer
Mr Pat Kinsella	MG132/MS033	pa.kinsella@ulster.ac.uk	75617	Computing Officer
Mr Bernard McGarry	MG132	bg.mcgarry@ulster.ac.uk	75644	Network Assistant
Mr Daniel Farren	MF143/MS030	db.farren@ulster.ac.uk	75008	Technician
Mr Neill McDonnell	MF143/MS030	n.mcdonnell@ulster.ac.uk	75554	Technician

Administrative & Secretarial Staff:

Name	Office	Email	Telephone	Comments
Ms Caroline Elvin	MS126	c.elvin@ulster.ac.uk	75605	Secretary to Head of School
Mr Stephen Friel	MG048	s.friel@ulster.ac.uk	75148	Secretary to the Courses' Coordinator
Ms Michelle Stewart	MG048	m.stewart@ulster.ac.uk	75382	Secretary

Day-to-day administration of each Programme is the responsibility of the Courses' Coordinator. All major decisions in the running of the Programme are taken at Course Committee meetings.

Course Committee

The Course Committee is essentially a committee formed by those members of academic staff who have teaching responsibilities on the Programme as well as the nominated Studies Advisors. The Course Committee reports to the Faculty's Academic Affairs Committee, which in turn reports to the Faculty of Computing and Engineering Board. This is the normal route for all of the Faculty's Course Committee meeting minutes.

Student progression (i.e. assessing the performances of students and determining whether or not they should be allowed to proceed to the next stage of the Programme) is the responsibility of the Board of Examiners. The Board of Examiners is essentially the Course Committee plus an External Examiner.

The Programme Committee meets on a regular basis - normally at least once per semester, although other special meetings may be arranged should a need be identified.

Support for Students

The University's **Teaching and Learning Support Charter** outlines the University's commitments to students and its responsibilities in relation to teaching and learning. This document is available for viewing at: <http://www.ulster.ac.uk/quality/qmau/t&l/supportcharter.pdf>

Student-Staff Consultative Committee

Part of the Course Committee meetings is devoted exclusively to the consideration of general student problems associated with the Programme which are reported from the Staff-Student Consultative Committee (SSCC). An elected student from each year of the Programme is invited to participate as Class Representatives on the SSCC. These elected representatives are invited to express the views of their peers in relation to the organisation and delivery of the Programme. In the past, issues addressed have included:

- Resourcing;
- Comments on the delivery assessment of each module;
- Workloads;
- General Feedback
- Other general programme-related matters

The Staff-Student Consultative Committee is a formal forum for students to express their opinions. The Faculty's policy is to encourage the early identification of problems and to bring these to the attention of staff as soon as possible.

Assessment

Criteria for assessing and grading course work

These are specific to each module and will be provided when each assignment is issued.

Assessment methods used may include:

Formal timed limited, unseen examination paper;
Open book class test;
Group project;
Individual project;
Written report;
Presentation.

SCHOOL OF COMPUTING AND INTELLIGENT SYSTEMS PROCEDURES FOR MONITORING STUDENT ATTENDANCE (MODULE OR OTHER NON-MODULE SPECIFIC EVENT)

- Attendance will be monitored on all modules across all years of study (undergraduate and postgraduate). Attendance at other non-module specific events (e.g. general tutorials, induction sessions, meetings with Advisors of Study) will also be monitored.
- Formal responsibility for attendance monitoring at the module level and the maintenance of evidence lies with the Module Coordinator. Module Coordinators will ensure that timely records of student attendance in an accessible format¹ are maintained. The Head of School will ensure that staff member(s) are nominated with corresponding responsibilities for the other designated non-module specific events.
- At the start of each module², Module Coordinators will ensure that students are:
 - a) reminded of their general attendance obligations³;
 - b) advised of any additional module-specific attendance requirements⁴;
 - c) reminded of the resources available to them for informing them of non-attendance or other related issues⁵;
 - d) advised as to how attendance records for the module will be assembled. They will identify (for example):
 - The range of classes at which attendance records will be taken (e.g. all lectures and lab classes; lab classes only; selected classes)
 - The mechanism(s) employed (e.g. manual roll-call, use of technology such as Turning Point, WebCT engagement monitoring, combination of methods).

Members of staff assigned corresponding responsibilities for the other designated non-module specific events will provide corresponding information for their events.

- All staff responsible for monitoring attendance will reflect regularly on the student attendance (and progress where appropriate) within their remit.
- Module Level or other non-Module specific event.

Where a member of staff identifies a student's attendance profile as giving rise for concern (without reasonable explanation and in the absence of appropriate supporting documentation), they will invoke the School's process for investigation of Student non-attendance. This will involve the member of staff advising the student's Studies Advisor and the student's Course/Subject Director of their concerns. The student's studies Advisor will first try to establish the reasons for the student's non-attendance. If this is unsuccessful the Studies advisor will inform the student's Course/Subject Director who will arrange for the reasons for the student's absences to be investigated, meeting with the student if appropriate. Should a student's attendance continue to give rise for concern the student's Course/Subject Director will advise the Head of School who will formally communicate with the student inviting them to discuss the reason(s)

¹ Note that a particular module may have students from a number of different courses/programmes.

² This may be done efficiently and consistently via a standard note within a Module Handout form or within WebCT.

³ For example, quoting the Student Charter, Course Regulations and other relevant sources

⁴ For example, talks by guest speakers, site visits etc. Submission of coursework may also be included.

⁵ For example, NA1 and EC1 forms which are available via the Student Portal

for the recurring absences. Details of any meetings with a student will be recorded and placed on the student's file.

- Failure to Submit Coursework

Where a student has failed to submit a piece of coursework (without reasonable explanation and in the absence of appropriate supporting documentation) the Module Coordinator will advise the student; and his/her Adviser of Studies and Course/Subject Director that a mark of zero has been awarded for the work as per University Policy. Module Coordinators should use non-submission of coursework in conjunction with a student's module attendance profile to support invoking the School's process for investigation of student non-attendance or referring the student to their Course/Subject Director (as described under Point 5).

- Staff will produce summary reports of attendance upon request from a Course/Subject Director and will also produce a final summary report on completion of the module.

PROGRAMME REGULATIONS

UNIVERSITY OF ULSTER SCHOOL OF COMPUTING AND INTELLIGENT SYSTEMS UNDERGRADUATE COURSE REGULATIONS

1. COURSE TITLES

COURSE TITLE	CODE
Bachelor of Engineering (with Honours) in Electronics and Computer Systems with Diploma in Industrial Studies/ Diploma in Area Studies FT/PT	24231/2228

Note part-time (P/T) versions of all the Bachelor of Engineering courses are available but for efficiency the University system only generates a related course code when a student enrolls on the programme.

2. MODE OF ATTENDANCE

Full Time and Part Time

3. DURATION

FULL-TIME SANDWICH:

Normally 4 years (6 semesters of study and placement year) or 5 years (if student enters at Level 3 (8 semesters of study and placement year).

FULL-TIME INTERCALARY: Normally 4 years (8 semesters of study including year of study abroad) or 5 years if the student enters at Level 3 (10 semesters of study including year of study abroad).

PART-TIME:

A minimum of 3 years and a maximum of 6 years (12 modules, at the rate of one or two per semester).

4. LOCATION

Magee Campus

5. FACULTY

Computing and Engineering

6. ADMISSION REQUIREMENTS

Applicants must:

- satisfy the University's general entry requirements and subjects must include Mathematics at least Grade C at GCSE level.

In addition, individual courses may have higher entry requirements set by the Course Committee and the Faculty.

or

- (b) provide evidence of their ability to undertake the programme through the accreditation of prior experiential learning.

See prospectus entry for each programme.

7. EXEMPTIONS

- 7.1 Studies pursued and examinations passed in respect of other qualifications awarded by the University or by another university or other educational institution, or evidence from the accreditation of prior experiential learning, may be accepted as exempting candidates from part of an approved programme provided that they shall register as students of the University of Ulster for modules amounting to at least the final third of the credit value of the award at the highest level.

8. PLACEMENT/STUDY ABROAD

During their placement year candidates are expected to adhere to the normal attendance practices at their place of employment. Each student is supervised by an industrial supervisor from the employing company and an academic supervisor from the Faculty.

Assessment of placement is on the basis of four elements. Overall performance on placement is collated by the Placement Tutor taking into account the elements listed above with weightings as indicated:

Academic Assessment (First & Second visits)	30%
Employer Assessment	20%
Professional Practice (Logbook & Technical Report)	20%
Reflection (Student Final Report)	30%

A candidate on placement who is unable to complete the placement year due to illness or other circumstances may, at the discretion of the Board of Examiners, be permitted to proceed to the final year of the course, and may be awarded the DIS provided that the minimum requirement of 25 weeks has been completed along with all assessments.

To be awarded the DAS, a candidate on placement abroad must complete the minimum requirement of 25 weeks and complete all assessments.

Exemption from Placement

Exemption from the placement requirement will be awarded only in the most exceptional circumstances. To be so exempted a candidate must satisfy the Board of Examiners that he/she has acquired appropriate work experience which the course committee considers equivalent to placement. A candidate must submit an application for exemption which must include a report (approximately 2,000 words) outlining the extent and level of the work experience already obtained, together with a report from his/her employer(s). The application will be assessed by a sub-committee consisting of the Course Director and the Placement Tutor, which will then make a recommendation to the Board of Examiners. An interview with the candidate may form part of the assessment process.

A candidate who is exempted from placement will not be eligible for the award of Diploma in Industrial Studies or Diploma in Area Studies.

Part-time students do not do a placement year, and are not eligible for the awards of DIS or DAS.

Unplaced Students

- A student who is unable to obtain placement may, at the discretion of the Board of Examiners and in exceptional circumstances only, be permitted to proceed directly to the final year of the course.
- A student who is unable to obtain placement may, at the discretion of the Board of Examiners, be permitted to obtain leave of absence in order to continue seeking a suitable placement.
- A student who is unable to obtain placement and who, in the opinion of the Board of Examiners, has not shown sufficient effort in obtaining placement may be required to withdraw from the course.
- A student who does not complete an appropriate placement will not be eligible for the award of Diploma in Industrial Studies or Diploma in Area Studies.

9. ATTENDANCE REQUIREMENTS

- 9.1 Students are expected to attend all classes associated with the programme and be punctual and regular in attendance.
- 9.2 A student who has not been in attendance for more than three days through illness or other cause must notify immediately their Studies Adviser or the Courses' Co-ordinator. The student shall state the reasons for the absence and whether it is likely to be prolonged. Where the absence is for a period of more than five working days, and is caused by illness which may affect their studies, the student shall provide appropriate medical certification in accordance with University regulations.
- 9.3 Students who are absent without good cause for a substantial proportion of classes may be required to discontinue studies, in accordance with University regulations.

10. RULES GOVERNING STUDENT CHOICE

- 10.1 Students enrolled for a Single Honours degree undertake an integrated programme of study primarily in Computer Science, Electronics and Computer Systems, Computer Games Development or Multimedia Computer Games. Students enrolled for a Major/Minor Honours degree undertake a programme of study with a two-thirds/one-third balance between two subjects. Students enrolled for a Joint Honours degree (Main) undertake a programme of study of equal weight in two subjects.

11. EXAMINATION AND ASSESSMENT

- 11.1 The performance of candidates shall be assessed by the Board of Examiners in accordance with the Regulations Governing Examinations in Programmes of Study.
- 11.2 Candidates shall be assessed in the modules for which they have enrolled in each year of study. At the discretion of the Board of Examiners candidates may be required to attend a viva voce examination.
- 11.3 Within each module candidates shall be assessed by a combination of coursework and examination in accordance with the table at section 18.
- 11.4 The pass mark for the module shall be 40%. Where a module is assessed by a combination of coursework and examination a minimum mark of 35% shall be achieved in each element.
- 11.5 FOR PROGRAMMES INCLUDING THE DIS/DAS AWARD: the pass mark for the placement/intercalary year is 50%; a mark of 40% is sufficient for progression to the next stage of the programme.

12. SUBMISSION OF COURSEWORK

- 12.1 Coursework shall be submitted by the dates specified by the Course Committee.
- 12.2 Students may seek prior consent from the Course Committee to submit coursework after the official deadline; such requests must be accompanied by a satisfactory explanation, and in the case of illness by a medical certificate. This application shall be made to the Courses' Co-ordinator.
- 12.3 Coursework submitted without consent after the deadline shall not normally be accepted.

13. PROGRESS

- 13.1 Subject to 14 hereof, candidates are required to pass all modules in each year of study in order to proceed to the next. Progress from semester 1 to semester 2 is automatic.

Part-time students may proceed from semester to semester, unless they have accumulated such failures as to prohibit them from proceeding to the subsequent level of the course. To proceed from level to level they are subject to the same regulations as full-time students.

14. CONSEQUENCES OF FAILURE

14.1 Candidates who fail to satisfy the Board of Examiners in assessment may be permitted at the discretion of the Board to re-present themselves as specified in 15.2 for one or more supplementary examination and repeat such coursework or other assessment requirements as shall be prescribed by the Board. Such candidates may be exempted at the discretion of the Board from the normal attendance requirements. Where candidates are required to repeat coursework or to take a supplementary examination the original mark in the failed coursework component or examination shall be replaced by a mark of 40% or the repeat mark whichever is the lower for the purpose of calculating the module result, except in the DIS/DAS year where the maximum mark allowed shall be 50%.

14.2 In each year, other than the final year, the consequences of failure shall normally be as follows:

Failure at the first attempt:

Failure in modules with an overall value up to and including 60 credit points	Repeat specified examinations and/or coursework in the failed modules (examinations August)
Failure in modules with an overall value of between 60 and 80 credit points	Repeat specified examinations and/or coursework in the failed first semester module(s) (examinations January) and of specified examinations and/or coursework in the second semester modules (examinations May) with or without attendance OR withdraw from the course.
Failure in modules with an overall value of more than 80 credit points	Withdraw from the course.
Failure by candidates in year 2 of sandwich courses	Exceptionally second year students on sandwich courses may be permitted to commence the placement period, pending a requirement to represent themselves for supplementary written examinations or to repeat coursework.
Failure at the Second Attempt Failure in modules with an overall value up to and including 20 credit points	Provided that the module(s) are not prerequisite(s), which must be passed, proceed to next year and repeat once only specified examination(s) and/or coursework in the failed module(s) at the next examination period (January or May).
Failure in modules with an overall value up to and including 40 credit points (except as above)	Repeat once only specified examination(s) and/or coursework in the failed module(s) at the next examination period (January or May or August if semester already repeated) with or without attendance (progress to next year not permitted).
Failure in modules with an overall value of more than 40 credit points	Withdraw from the course.

Consequences of failure in placement year (DIS):

Failure at the First Attempt:

Failure in project	Resubmit project by a date specified by the Course Committee. (Maximum mark not to exceed 50%.)
Failure in placement	Repeat <i>once only</i> all or part of placement.

Failure at the Second Attempt:

Failure in project	Repeat <i>once only</i> the project. (Maximum mark not to exceed 50%.)
Failure in placement	Withdraw from the Programme.

Consequences of failure in intercalary year (DAS)

Failure at the First Attempt	Repeat <i>once only</i> the study period in whole or in part OR take specified examinations and/or coursework. (Maximum mark not to exceed 50%.)
Failure at the Second Attempt	Withdraw from the programme OR repeat <i>once only</i> specified examinations and/or coursework. (Maximum mark not to exceed 50%.)

14.3 Failure in the Final Year (Honours degree)

Failure in modules with an overall value up to and including 40 credit points	Repeat <i>once only</i> specified examination(s) and/or coursework in the failed module(s) in consideration for Honours classification (examinations August).
Failure in modules with an overall value of more than 40 credit points	Withdraw from the programme. Candidate may be considered for a linked AB exit award if appropriate.

15. CLASSIFICATION OF FINAL RESULT

15.1 The table at section 18 indicates the contribution of each final year module to the final award. The weighting of each module's contribution to the overall mark shall be determined by its credit value.

NOTE THAT: LEVELS 3/4/5 ASSESSMENT RESULTS DO NOT CONTRIBUTE TO THE FINAL HONOURS DEGREE RESULT.

15.2 Classification of Final Result (Honours degree)

The following percentages shall be used to determine candidates' overall gradings:

Class I	At least 70%
Class II (division i) (Ili)	At least 60% and less than 70%
Class II (division ii) (Ilii)	At least 50% and less than 60%
Class III	At least 40% and less than 50%

15.3 Classification of Associate Bachelor's Exit Award

The overall results of candidates shall be based on their performance in all Level 5 modules only. The weighting of each module's contribution to the overall mark shall be determined by its credit value.

The following shall be the minimum percentages acceptable in determining the overall gradings of candidates.

Pass with Commendation	60%
Pass	40%

15.3.1 Classification of Diploma in Computer Systems

(Exit award for Level 3: BSc Hons Computer Science and BEng Hons Electronics and Computer Systems only)

All modules contribute to the final result. The table at section 18 indicates the contribution of each module to the exit award. The weighting of each module's contribution to the overall mark shall be determined by its credit value.

Candidates who successfully complete the year will normally proceed to Year 1 of the BSc Hons Computer Science programme or the BEng Hons Electronics and Computer Systems programme. Candidates who wish to discontinue their studies may leave with a Diploma in Computer Systems.

Where candidates choose to take the exit award, the following shall be the minimum percentages acceptable in determining the overall gradings of candidates.

Pass with Commendation	60%
Pass	40%

15.3.2 Award of Diploma in Industrial Studies OR Diploma in Area Studies

The following shall be the minimum percentages used in determining the overall gradings of candidates in the Diploma.

Pass with Commendation	70%
Pass	50%

16. **ILLNESS AND OTHER EXTENUATING CIRCUMSTANCES**

16.1 In any year other than final year:

The Board of Examiners may in the case of candidates who are prevented by illness or other sufficient cause from taking or completing the whole or part of the assessment during the programme, or whose results are substantially affected by illness or other sufficient cause, permit the candidates to complete, take, or repeat the assessment in one or more modules at an approved subsequent date.

16.2 Final year (Honours Degree):

The Board of Examiners may in the case of candidates who are prevented by illness or other sufficient cause from taking or completing the whole or part of the final stage assessment or whose results are substantially affected by illness or other sufficient cause:

(a) permit the candidate to complete, take, or repeat as candidates for the Honours degree, the assessment in one or more modules at an approved subsequent date **or**

(b) deem the candidate to have passed and recommend the award of an Aegrotat Honours Degree.

16.3 Before an Aegrotat award is recommended a candidate must have indicated that he or she is willing to accept the award.

17. REVISIONS TO REGULATIONS

These regulations may be revised during the student's period of registration in accordance with the procedures approved by Senate.

18 TABLE

Year	Semester	Level	Module Title	Code	Credit Value	Status Compulsory (c) Optional (o)	Assessment Methods		Contribution to the overall mark of the Final Award
							% Examination	% Coursework	
0	1	3	Introduction to Internet and Multimedia Technology	COM008	20	C		100	0
0	1	3	Introduction to Programming	COM009	20	C	50	50	0
0	1	3	Introduction to Mathematics for Computing	COM010	20	C	50	50	0
0	2	3	Introduction to Electronics	COM011	20	C	50	50	0
0	2	3	Introduction to Computer Systems	COM012	20	C	50	50	0
0	2	4	Business Information Systems	COM112	20	C	50	50	0
1	1	4	Algorithmic Programming I	COM136	20	C		100	0
1	1	4	Mathematics 1	COM121	20	C	50	50	0
1	1	4	Systems Analysis and Data Management	COM178	20	C	50	50	0
1	2	4	Computer Hardware and Organisation	COM167	20	C	50	50	0
1	2	4	Algorithmic Programming II	COM139	20	C	50	50	0
1	2	4	Analogue and Digital Electronics	COM119	20	C	50	50	0
2	1	5	Object Oriented Programming	COM326	20	C	50	50	0
2	1	5	Professional Issues	COM417	10	C		100	0
2	1	5	Informatics Entrepreneurship	COM418	10	C		100	0
2	1	5	Mathematics II	COM420	20	C	75	25	0
2	2	5	Electronics Systems Design	COM422	20	C	75	25	0
2	2	5	Computer Networks and Operating Systems	COM320	20	C	75	25	0
2	2	5	Microprocessor Systems Design	EEE315	20	C		100	0
3	1/2/3	5	Industrial Placement	COM367	60	C		100	DIS/DAS
4	1 and 2	6	Final Year Project	EEE521	30	C		100	25%
4	1	6	Visual Programming	COM622	10	C		100	8.3%
4	1	6	Intelligent Systems	COM623	20	O	75	25	16.6%
4	1	6	Network Games Development	COM588	20	O		100	16.6%
4	1	6	Programmable Logic Systems	EEE540	20	O	50	50	16.6%
4	2	6	Concurrent and Distributed Systems	COM561	20	O	75	25	16.6%
4	2	6	Wireless Communication Systems	COM586	20	O	75	25	16.6%

Year	Semester	Level		Code	Credit Value	Status Compulsory (c) Optional (o)	Assessment Methods		Contribution to the overall mark of the Final Award
							% Examination	% Coursework	
4	2	6	Mobile Robotics	COM596	20	O	75	25	16.6%
4	2	6	Embedded Systems Design	EEE502	20	O	50	50	16.6%

